THE UNITED STATES PATENT AND TRADEMARK OFFICE

REVOCATION AND NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS

I, Dr. Graham Fisher, Director of Intellectual Property of MEMC Electronic Materials, Inc., the Assignee of the entire right, title, and interest in the U.S. Patent Application(s) and/or Patent(s) identified on the attached Schedule A, hereby revoke all previous powers of attorney or authorizations of agent given and do hereby appoint the attorneys or agents associated with the following Customer Number, with full power of substitution and revocation, to prosecute and transact all business in the Patent and Trademark Office connected therewith for the U.S. Patent Application(s) and/or Patent(s) listed in the attached Schedule A:

Customer Number: 76681

Please direct all correspondence in connection with said U.S. Patent Application(s) and/or Patent(s) to:

Customer Number: 76681

5/13/2008

Respectfully submitted,

Dr. Graham Fisher

Director of Intellectual Property MEMC Electronic Materials, Inc.

THE UNITED STATES PATENT AND TRADEMARK OFFICE

STATEMENT UNDER 37 CFR 3.73(b)

MEMC Electronic Materials, Inc., a Delaware Corporation, pursuant to 37 CFR 3.73(b), hereby states that it is the Assignee of the entire right, title, and interest in U.S. Patent Application(s) and/or Patent(s) on the attached Schedule A.

The entire rights, title, and interest in the aforementioned Patent Application(s) and/or Patent(s) were conveyed to *MEMC Electronic Materials, Inc.* via Assignment(s) recorded with the United States Patent and Trademark Office at the *Reel/Frame Numbers on the attached Schedule A.*

The undersigned, Dr. Graham Fisher, Director of Intellectual Property, has full authorization to act on behalf of Assignee MEMC Electronic Materials, Inc.

Date: 5/13/2008

Respectfully submitted,

Dr. Graham Fisher

Director of Intellectual Property MEMC Electronic Materials, Inc.

APPENDIX A Owned by MEMC Electronic Materials, Inc.

CONF. NO	PUBLICATION NO. & DATE	SERIAL NO. FILING DATE	PATENT NO. ISSUE DATE	CURRENT OWNER/ ASSIGNEE	REEL AND FRAME NO.	TITLE
	US-2002-0083889-A1 7/4/2002	10/073,506 2/11/2002	6.743.289 6/1/2004	MEMC Electronic Materials, Inc	Division of 09/416,998 recorded at 010818/0677	THERMAL ANNEALING PROCESS FOR PRODUCING LOW DEFECT DENSITY SINGLE CRYSTAL SILICON
	US-2002-0134302-A1 9/26/2002	09/815,508 3/23/2001	6,579,362 6/17/2003	MEMC Electronic Materials, Inc	011897/0283	HEAT SHIELD ASSEMBLY FOR CRYSTAL PULLER
		09/608,304 8/30/2000	6,435,474 8/20/2002	MEMC Electronic Materials, Inc.	010938/0274	NON-CONTAMINATING GAS-TIGHT VALVE FOR SEMI- CONDUCTOR APPLICATIONS
		09/505,269 2/16/2000	6,479,386 11/12/2002	MEMC Electronic Materials, Inc.	010883/0103	PROCESS FOR RECUDING SURFACE VARIATIONS FOR POLISHED WAFER
	US-2002-0007779-A1 1/24/2002	09/475,320 12/30/1999	6,638,357 10/28/2003	MEMC Electronic Materials, inc	010688/0082	METHOD FOR REVEALING AGGLOMERATED INTRINSIC POINT DEFECTS IN SEMICONDUCTOR CRYSTALS
		09/502,340 2/10/2000	6,776,840 8/17/2004	MEMC Electronic Materials, Inc.	010585/0457	METHOD OF CONTROLLING DIAMETER OF A SILICON CRYSTAL IN A LOCKED SEED LIFT GROWTH PROCESS
		09/489,481 1/21/2000	6,315,828	MEMC Electronic Materials, Inc	Continuation of 09/167,747 recorded at 009612/0506	CONTINUOUS OXIDATION PROCESS FOR CRYSTAL PULLING APPARATUS
	US-2003-0008421-A1 1/9/2003	09/989,200 11/21/2001	6,485,992 11/26/2002	MEMC Electronic Materials, Inc	012627/0404	PROCESS FOR MAKING WAFERS FOR ION IMPLANTATION MONITORING
		09/566,890 5/8/2000	6,444,027 9/3/2002	MEMC Electronic Materials, Inc.	011003/0198	MODIFIED SUSCEPTOR FOR USE IN CHEMICAL VAPOR DEPOSITION PROCESS
9780	US-2003-0041799-A1 3/B/2003	10/229,415 8/28/2002	6.852,850 11/25/2003	MEMC Electronic Materials, Inc	Continuation of 09/566.890 recorded at 011003/0198	MODIFIED SUSCEPTOR FOR USE IN CHEMICAL VAPOR DEPOSITION PROCESS
		09/723,847 11/28/2000	8,515,742 2/4/2003	MEMC Electronic Materials, Inc	011320/0948	DEFECT CLASSIFICATION USING SCATTERED LIGHT INTENSITIES
	US-2001-0037761-A1 11/8/2001	09/752.222 12/29/2000	6.596.095 7/22/2003	MEMC Electronic Materials, Inc	011662/0217	AN EPITAXIAL SILICON WAFER FREE FROM AUTODOPING AND BACKSIDE HALO AND A METHOD AND APPARATUS FOR THE PREPARATION THEREOF
		09/633,958 8/8/2000	6,454,635 9/24/2002	MEMC Electronic Materials, Inc	011214/0312	METHOD AND APPARATUS FOR A WAFER CARRIER HAVING AN INSERT
		09/503,566 2/14/2000	6,344,083 2/5/2002	MEMC Electronic Materials, Inc.	010783/0683	PROCESS FOR PRODUCING A SILICON MELT
	US-2002-0020339-A1 2/21/2002	09/943.600 8/30/2001	6,652,645 11/25/2003	MEMC Electronic Materials, Inc	Continuation of 09/503,566 recorded at 010783/0683	PROCESS FOR PREPARING A SILICON MELT
	US-2002-0083887-A1 7/4/2002	10/036,875 10/23/2001	6,749,683 6/15/2004	MEMC Electronic Materials, Inc.	01722/0302	PROCESS FOR PRODUCING A SILICON MELT
		09/521,525 3/8/2000	6,350,312	MEMC Electronic	010884/0146 and 012139/0404	STRONTIUM DOPING OF MOLTEN SILICON FOR USE IN